

REMARKS

Applicants have rewritten Claims 1-15. No new matter has been added as a result of this amendment. The claims have been rewritten for grammatical purposes only to comply with US requirements. For example, the terms containing and comprising being equivalent, claims reciting multiple references to elements containing or comprising other elements have been modified to recite that the elements comprise other elements, all instances of "x and/or y" have been rewritten in format acceptable to indicate that at least one of x or y can take place, and some formatting, which is unable to be shown using markup, has been changed. If for any reason the Examiner is unable to allow the application in the next Office Action and believes that a telephone interview would be helpful to resolve any remaining issues, he is respectfully requested to contact the undersigned.

Respectfully submitted,



Anthony P. Curtis, Ph.D.
Registration No. 46,193
Agent for Applicants

BRINKS HOFER GILSON & LIONE
P.O. BOX 10395
CHICAGO, ILLINOIS 60610
(312) 321-4200

In1255DE

Abstract

Integrated circuit arrangement having capacitors and having preferably planar transistors and fabrication method

An explanation is given, inter alia, of an integrated circuit arrangement (140), which contains a preferably planar transistor (142) and a capacitor (144). The bottom electrode of the capacitor (144) is arranged together with a channel region of the transistor (142) in an SOI substrate. The circuit arrangement (140) is simple to fabricate and has outstanding electronic properties.

(Figure 13)